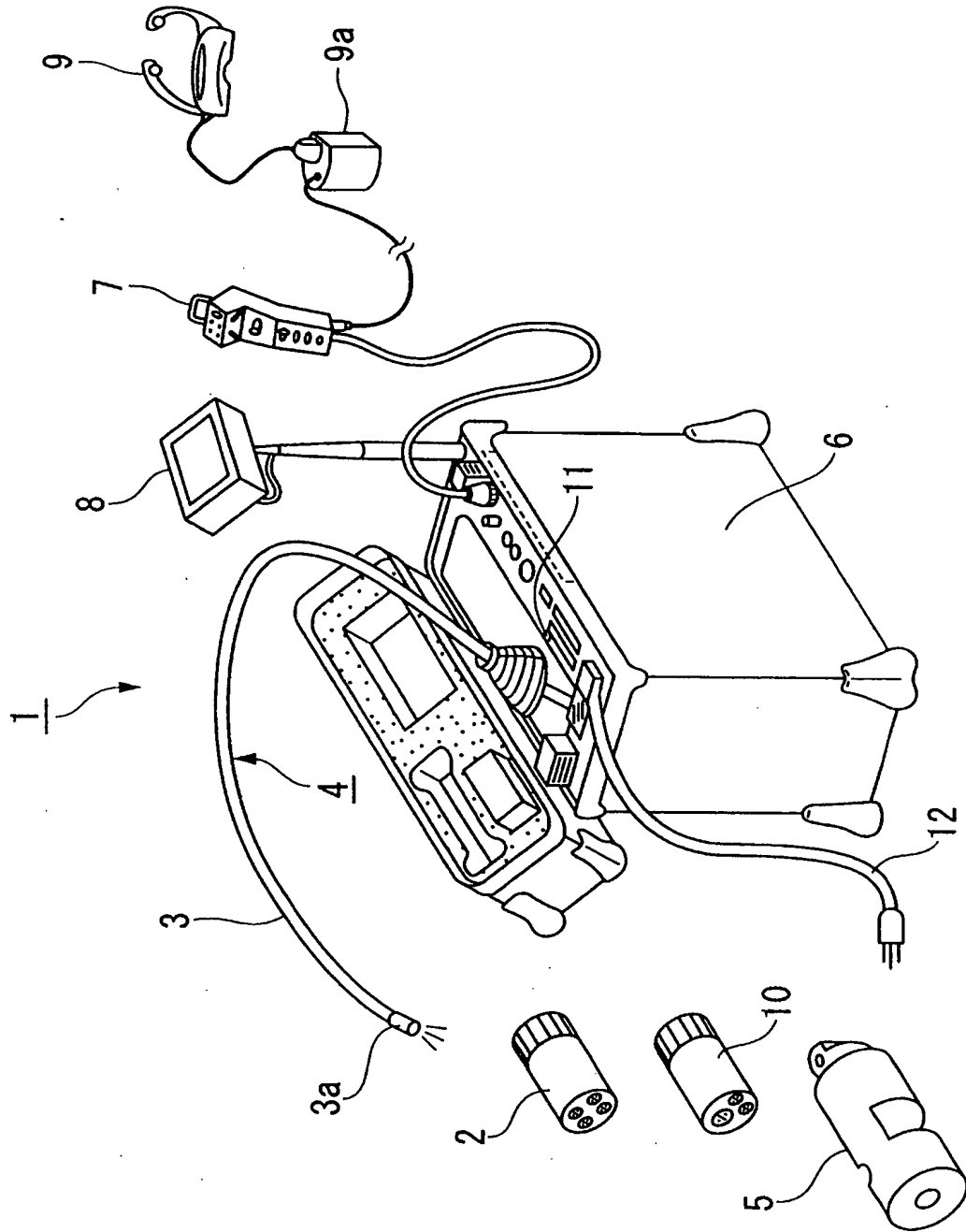


FIG. 1



The diagram illustrates a system 1, enclosed within a dashed boundary 6. The system's core components include a CPU 18, a ROM 19, and a RAM 20, which are interconnected. A LIGHT SOURCE 15 is connected to a CCU 17. The CCU 17 is linked to both a VIDEO SIGNAL PROCESSING CIRCUIT 23 and a SOUND SIGNAL PROCESSING CIRCUIT 22. The VIDEO SIGNAL PROCESSING CIRCUIT 23 is connected to a camera 8 and a display 11. The SOUND SIGNAL PROCESSING CIRCUIT 22 is connected to a microphone 28 and a speaker 22a. Additionally, the system features an IDENTIFICATION CIRCUIT 51, a PC CARD I/F 21a, a USB I/F 21b, and an RS-232C I/F 21c. Various external devices are connected to the system: devices 2, 3, 4, and 5 are connected to the LIGHT SOURCE 15; device 7 is connected to the IDENTIFICATION CIRCUIT 51; and a PC 25, a device 26, and a device 27 are connected to the PC CARD I/F 21a. A device 28 is connected to the microphone input of the SOUND SIGNAL PROCESSING CIRCUIT 22.

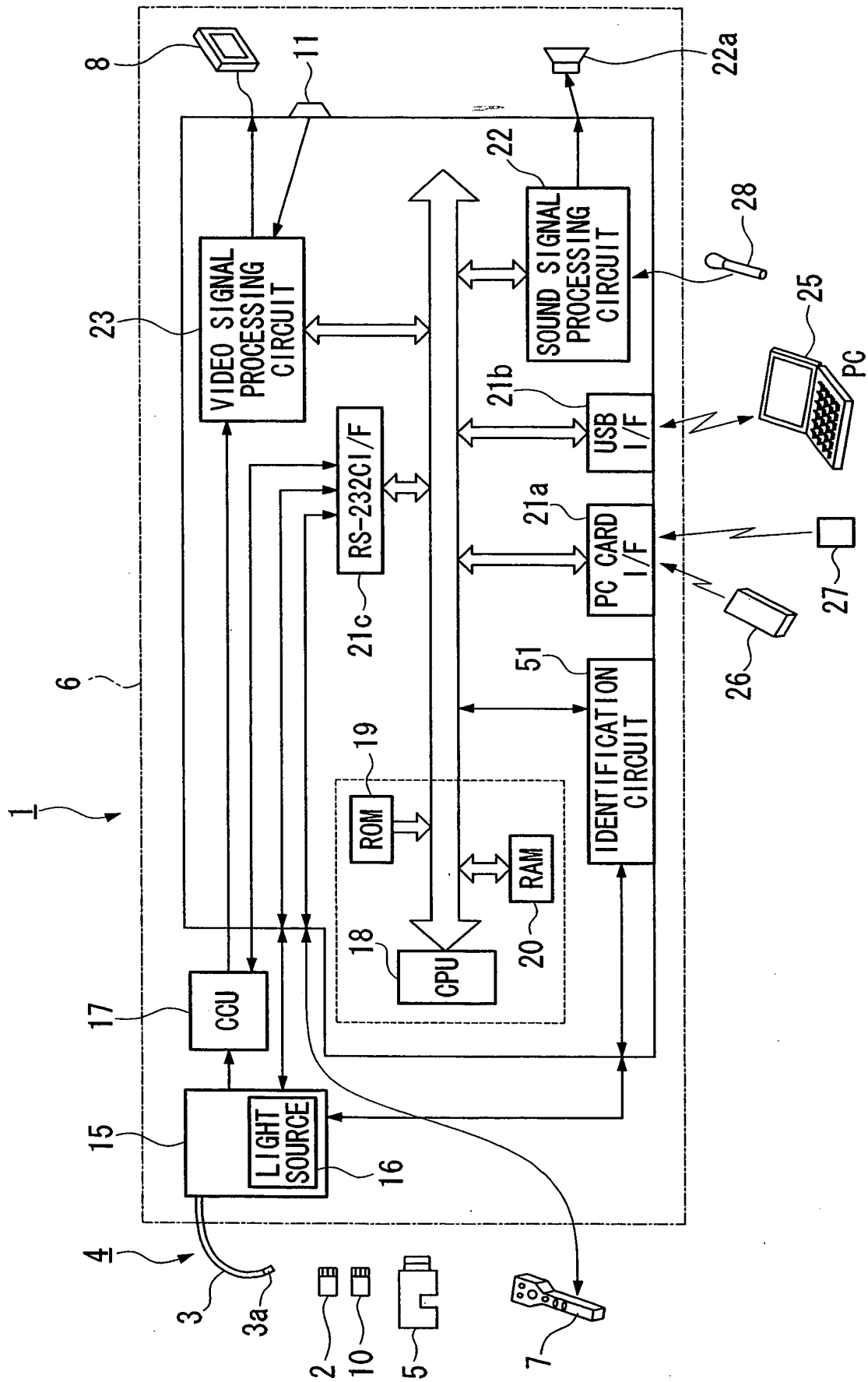


FIG. 3

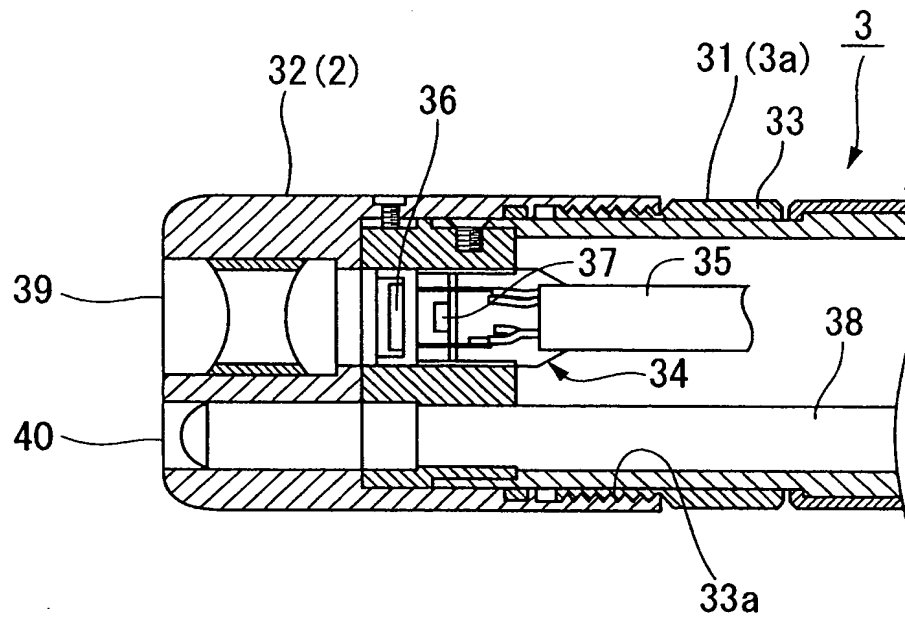


FIG. 4

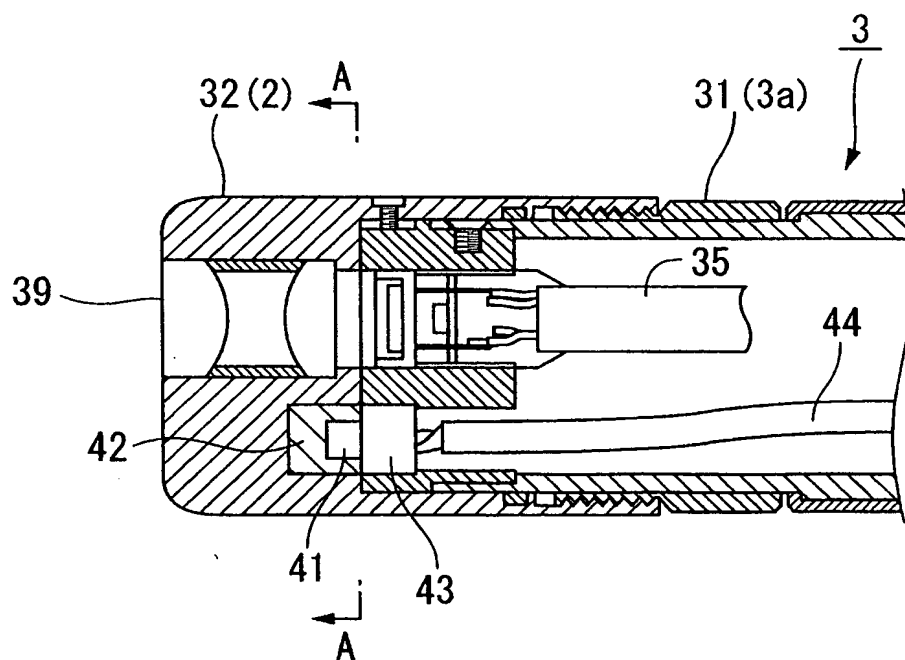


FIG. 5

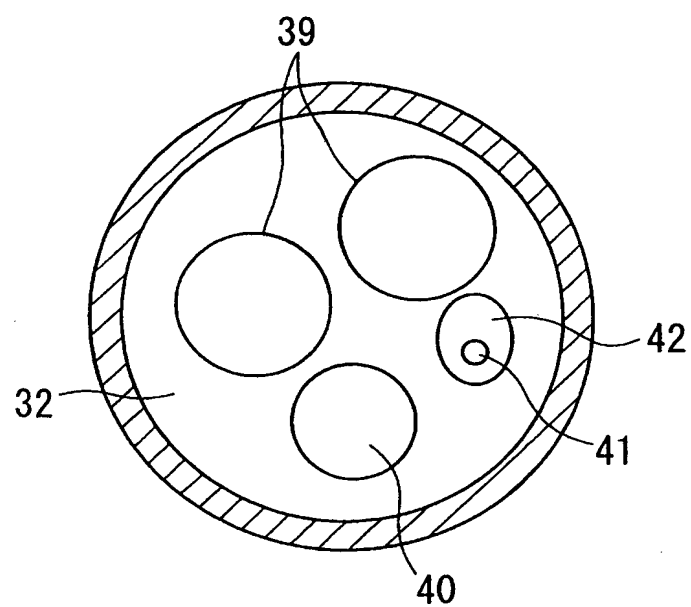


FIG. 6

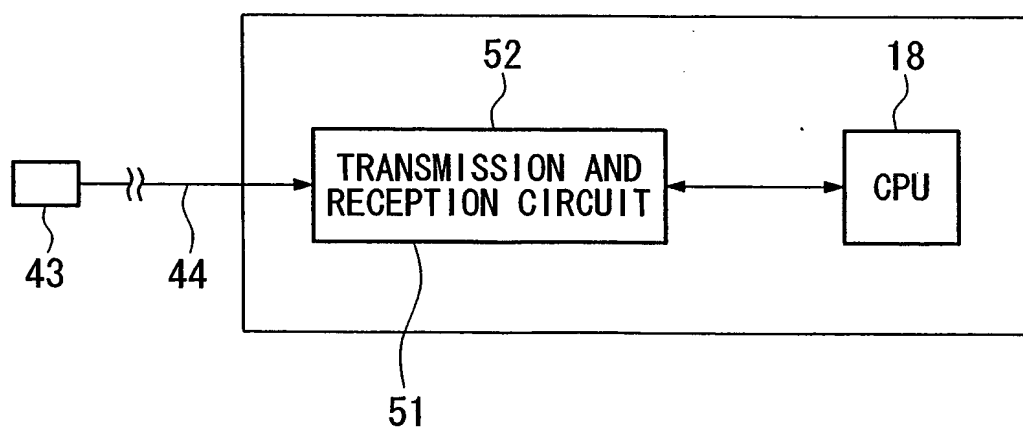


FIG. 7

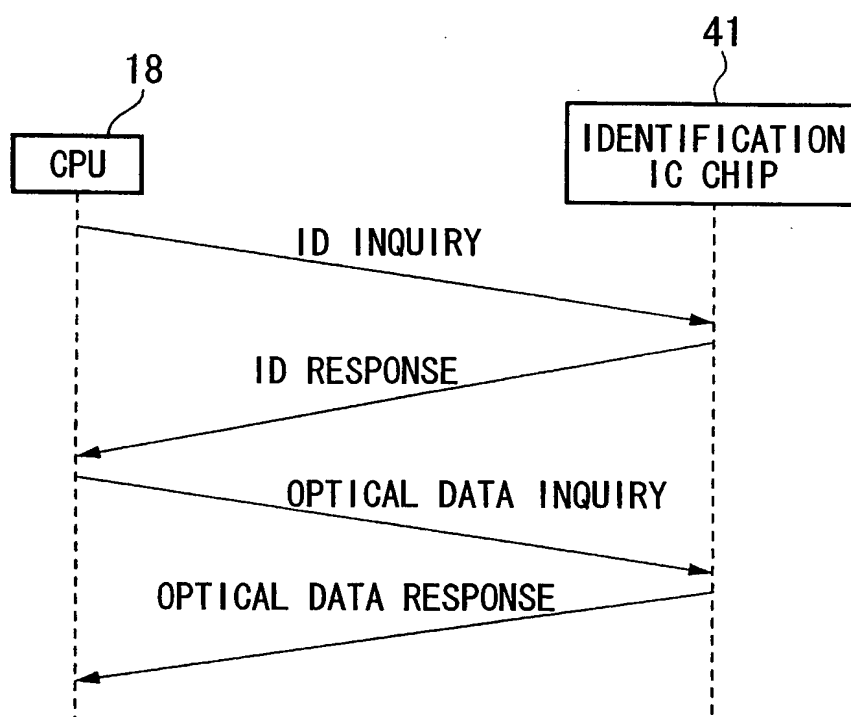


FIG. 8

ITEMS	DIRECTION	FORMAT
ID INQUIRY	CPU->IC	ID[EOF]
OPTICAL DATA INQUIRY	CPU->IC	DATA[EOF]
ID	IC->CPU	1234[EOF]
OPTICAL DATA	IC->CPU	120,320,240[EOF]

FIG. 9

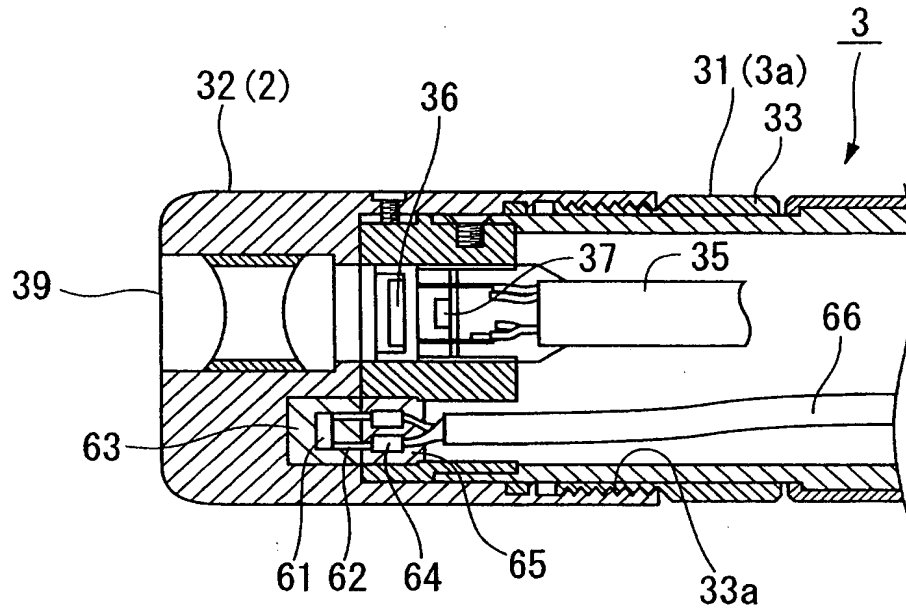


FIG. 10

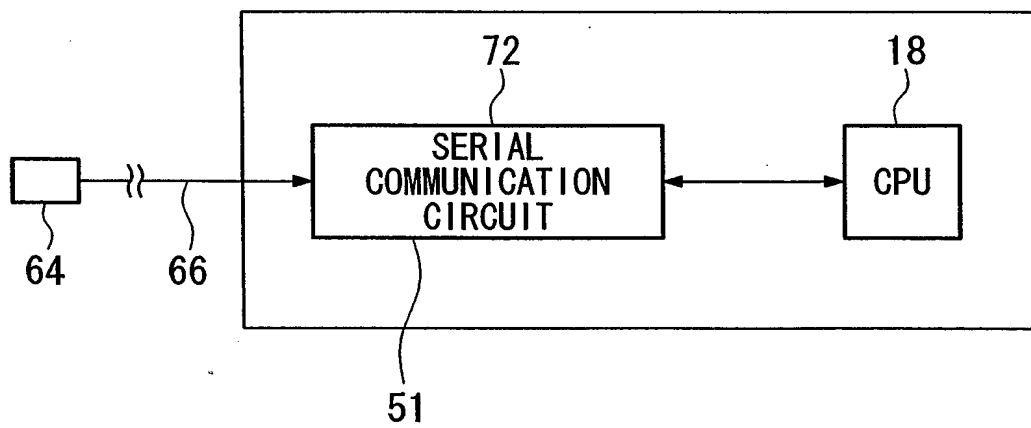


FIG. 11

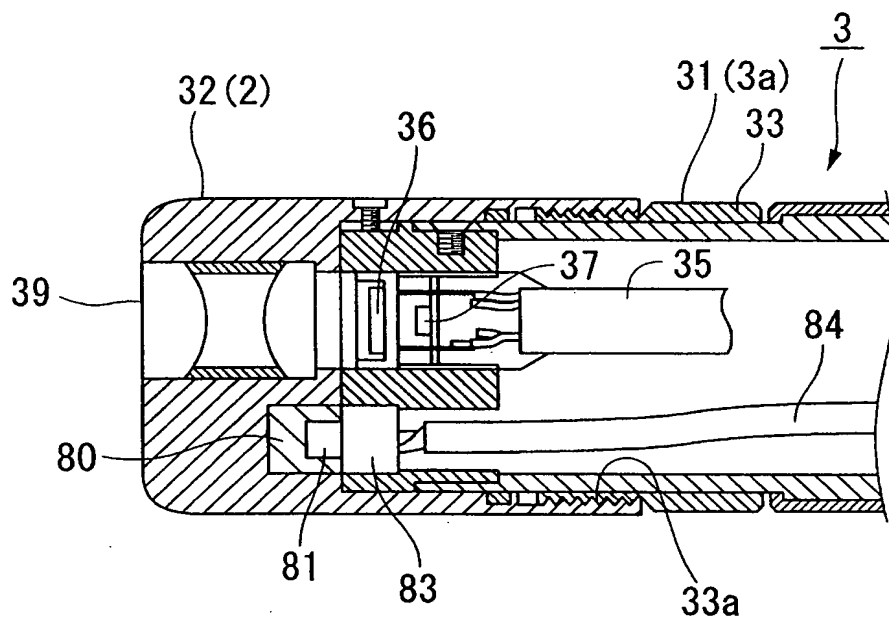


FIG. 12

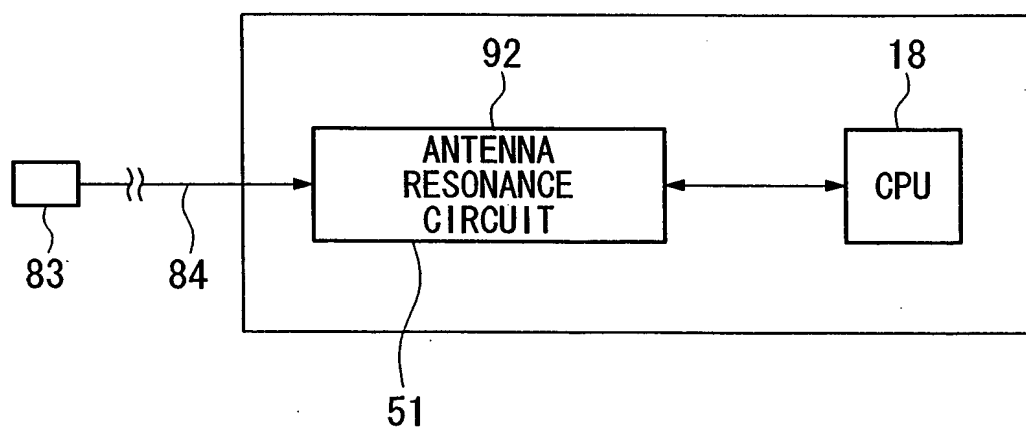


FIG. 13

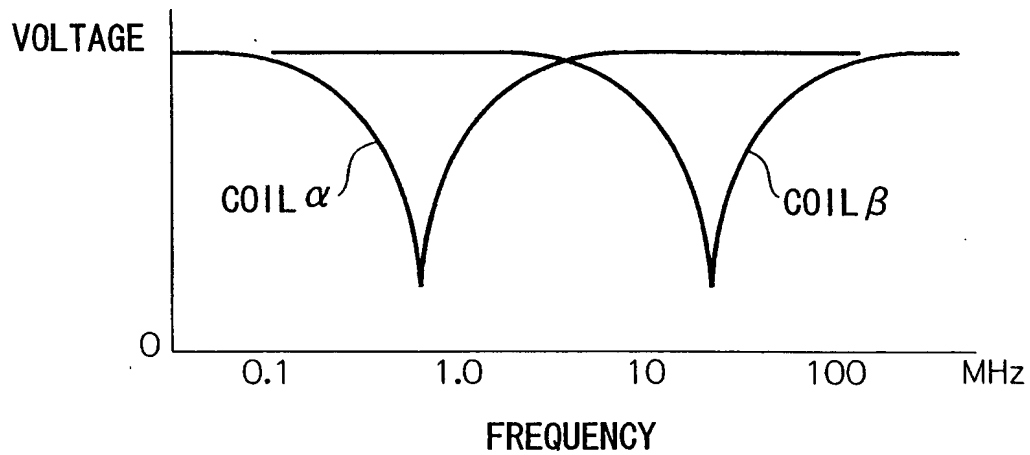


FIG. 14

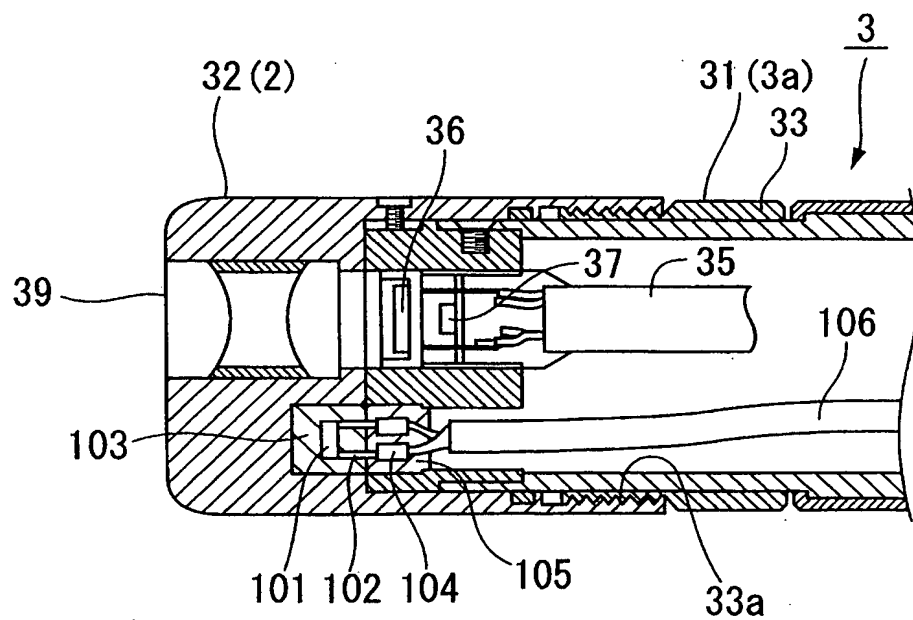




FIG. 15

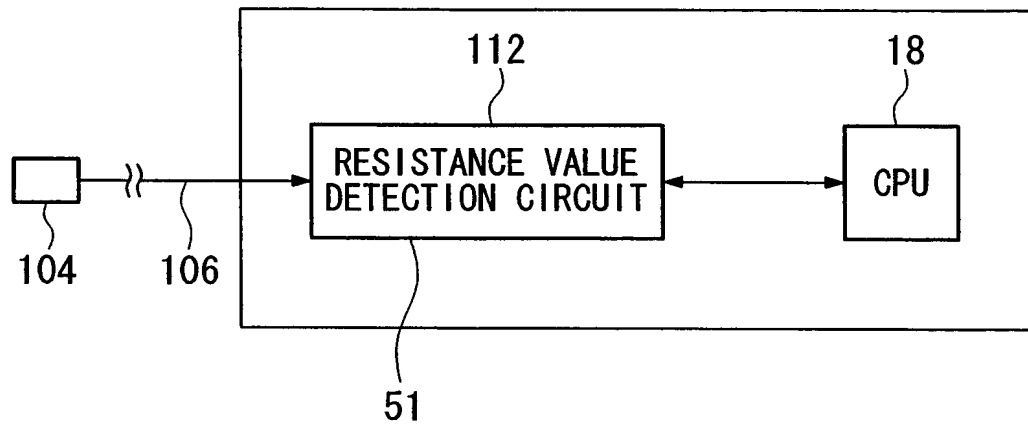


FIG. 16

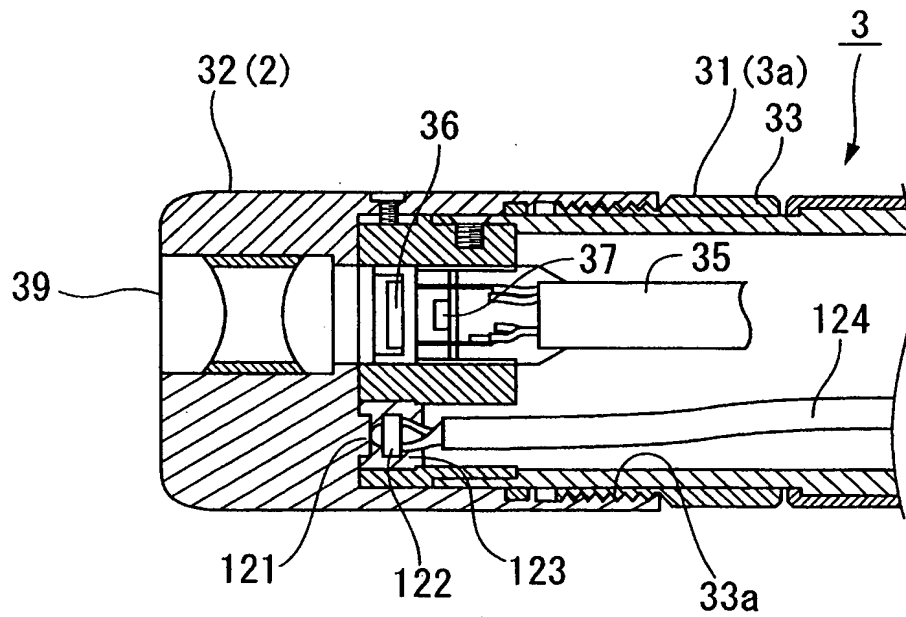


FIG. 17

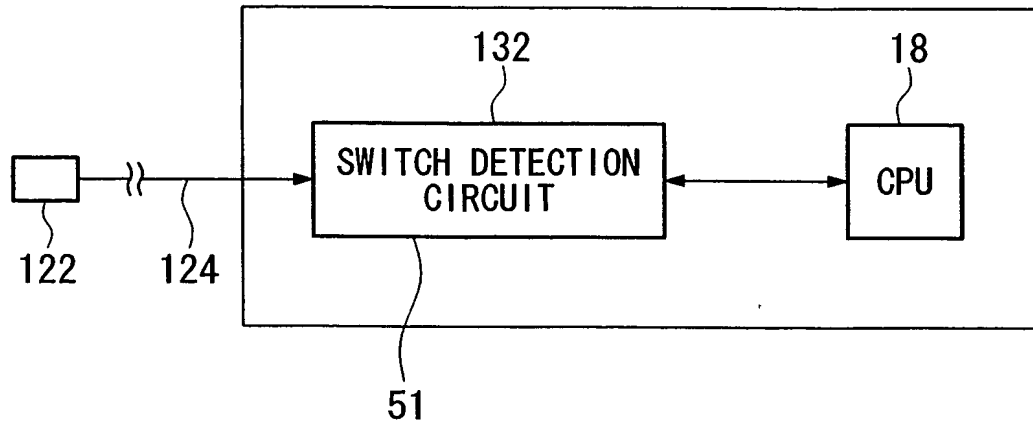


FIG. 18

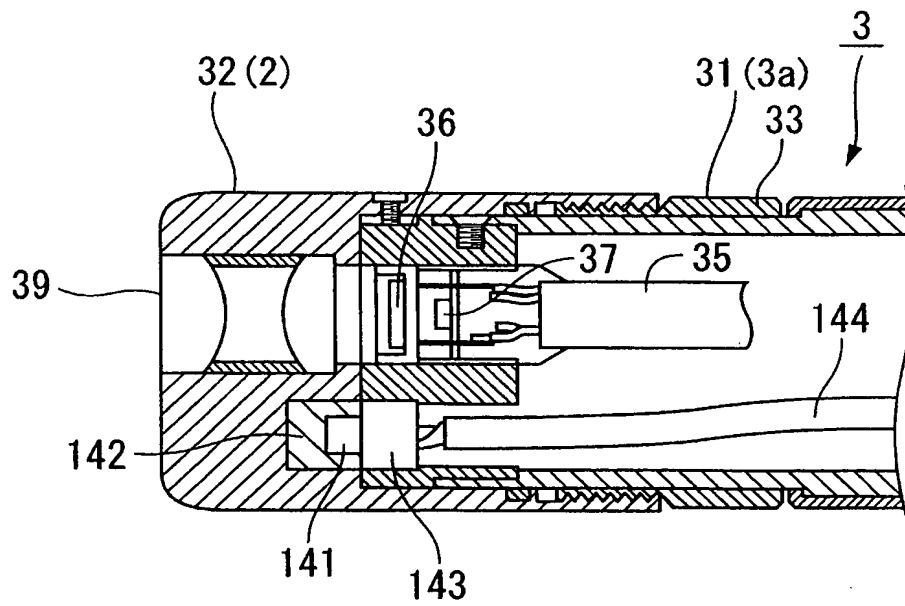


FIG. 19

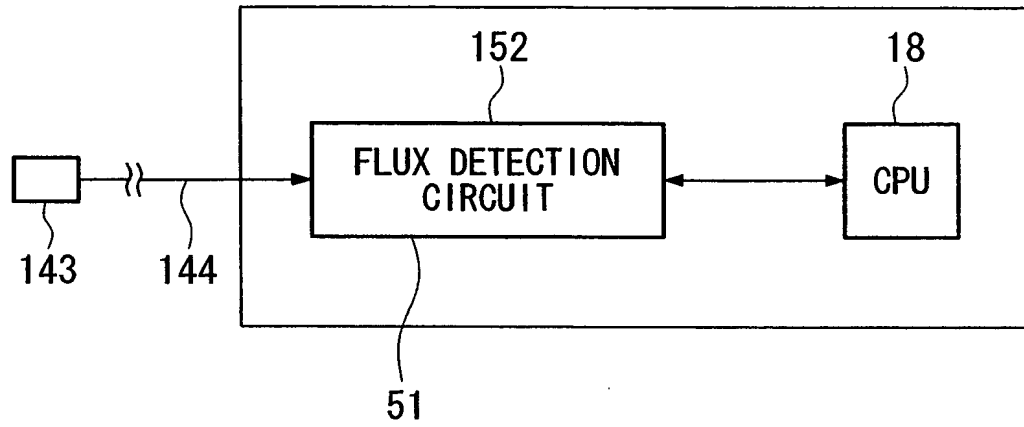


FIG. 20

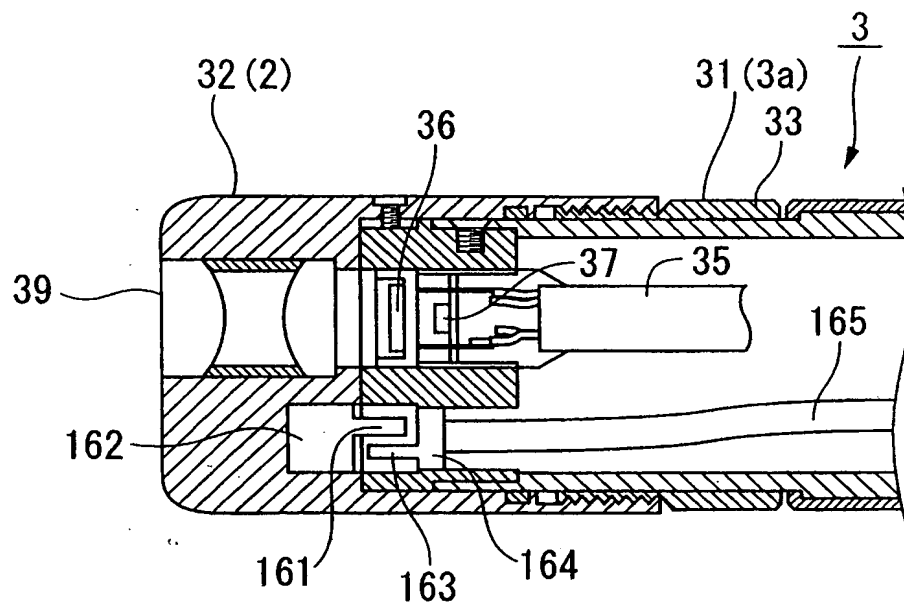
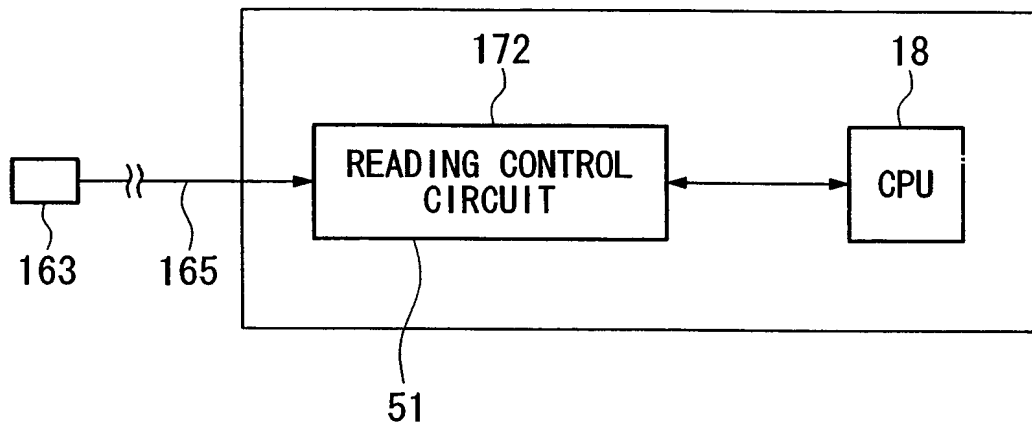


FIG. 21



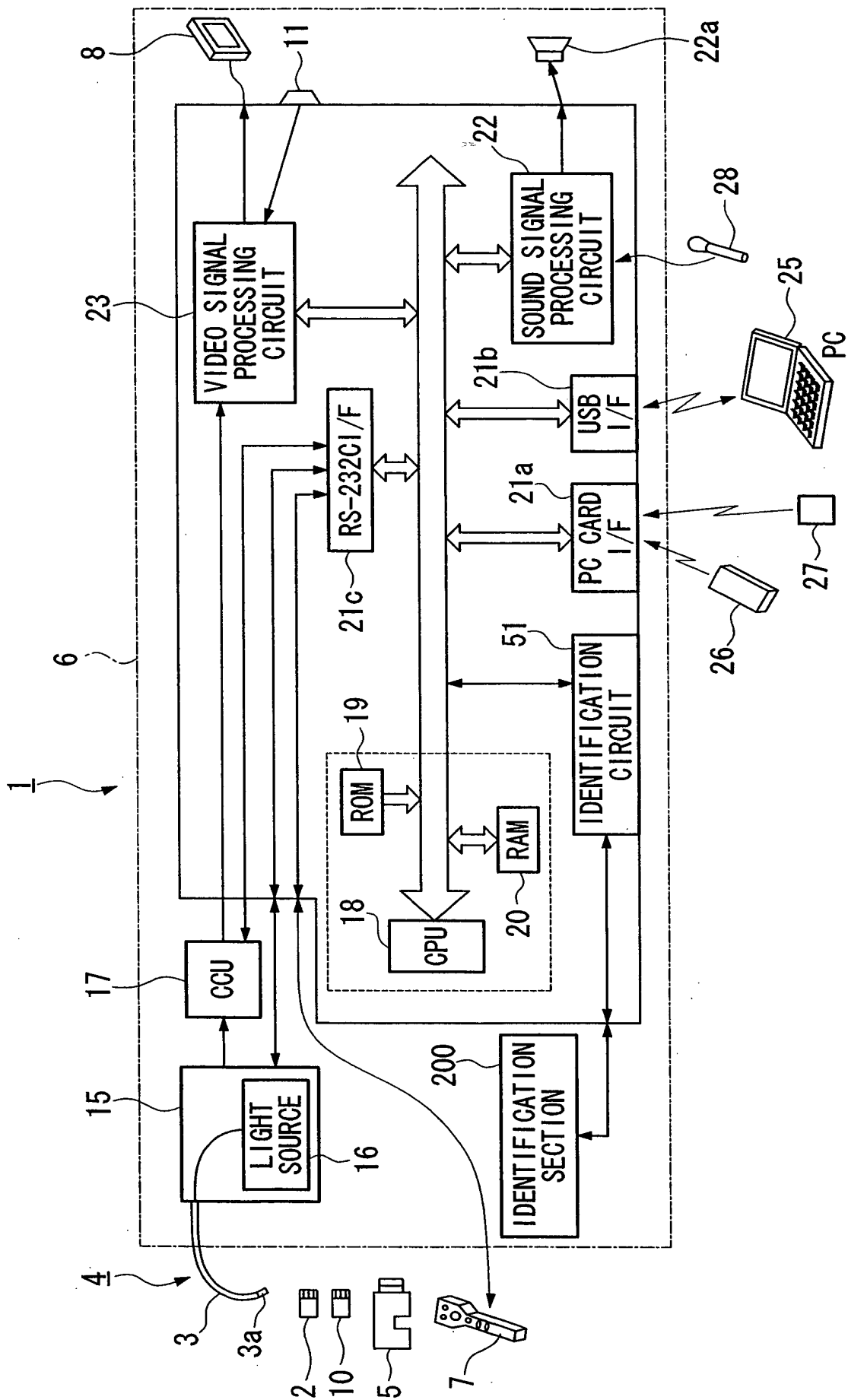
[illegible]

FIG. 23

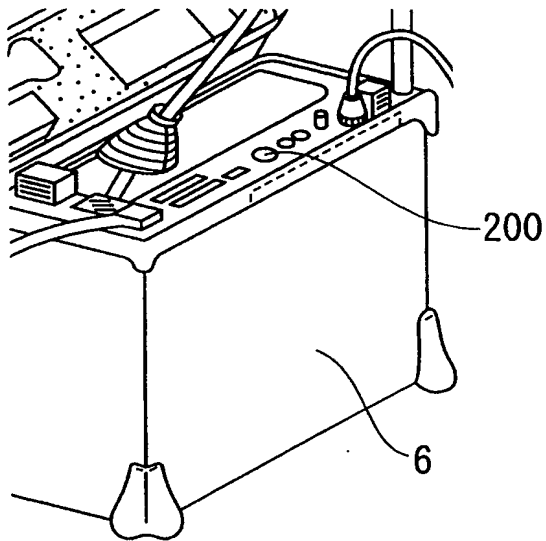


FIG. 24

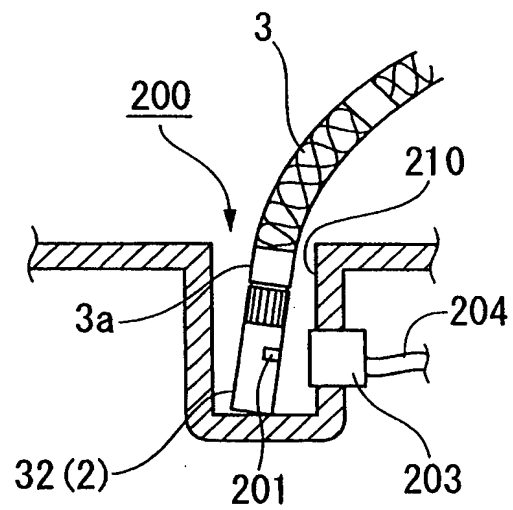


FIG. 25

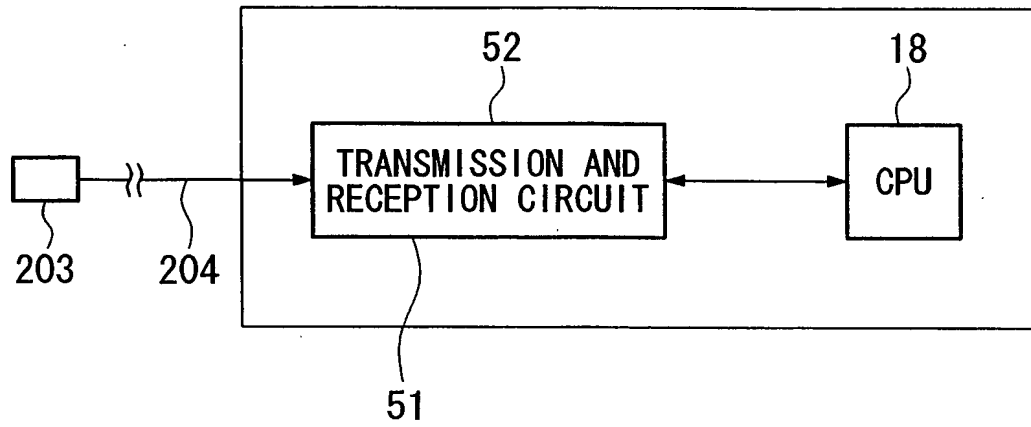


FIG. 26

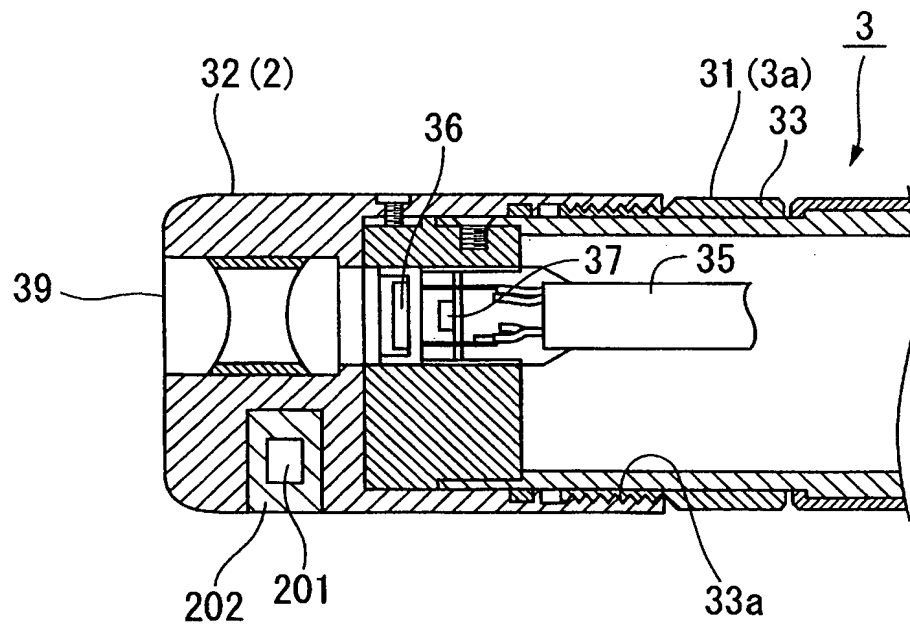


FIG. 27

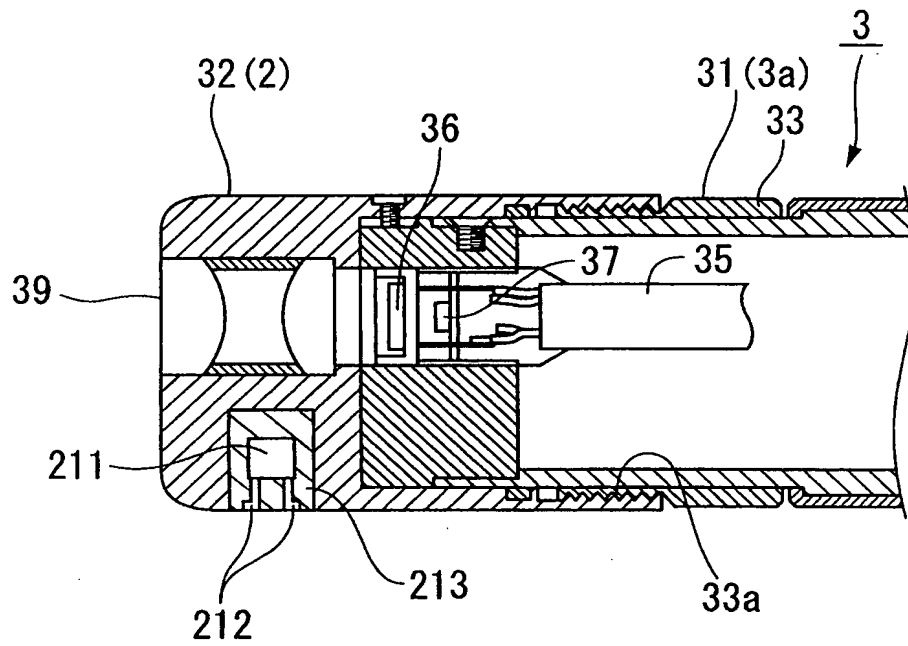


FIG. 28

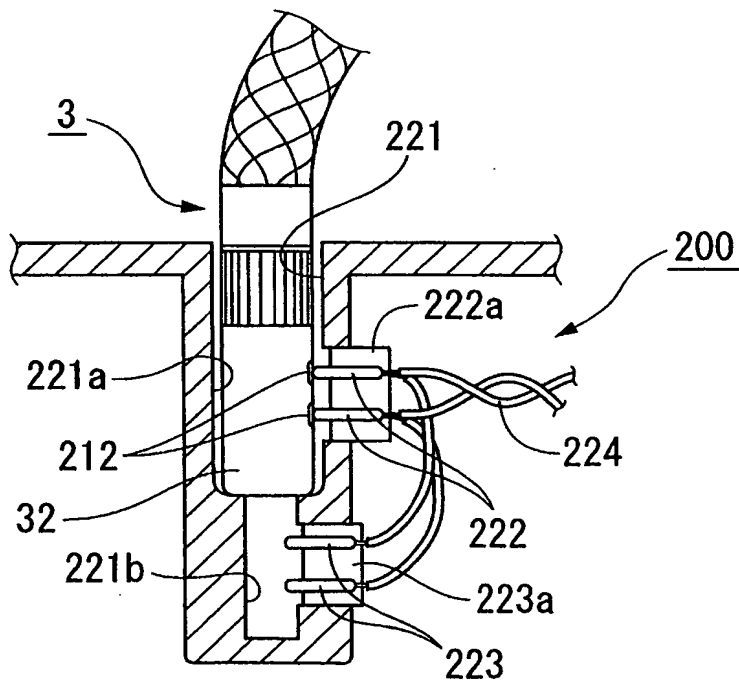




FIG. 29

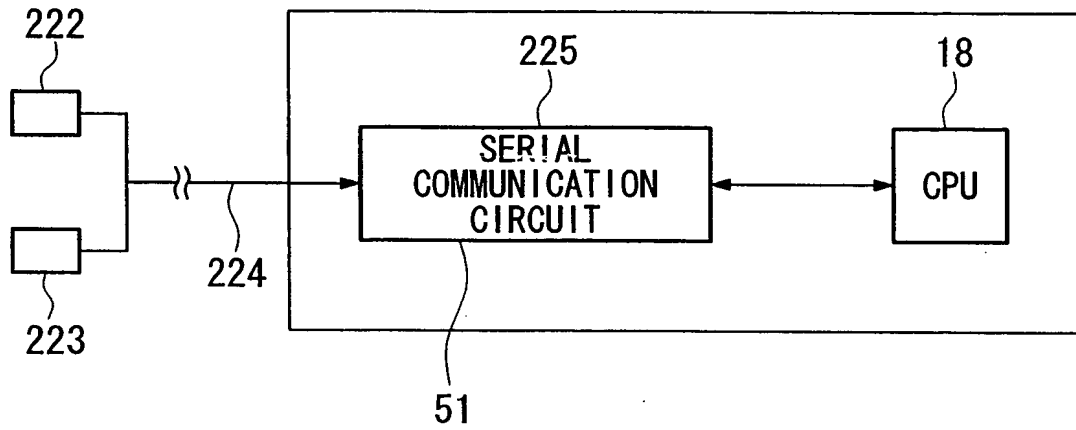


FIG. 30

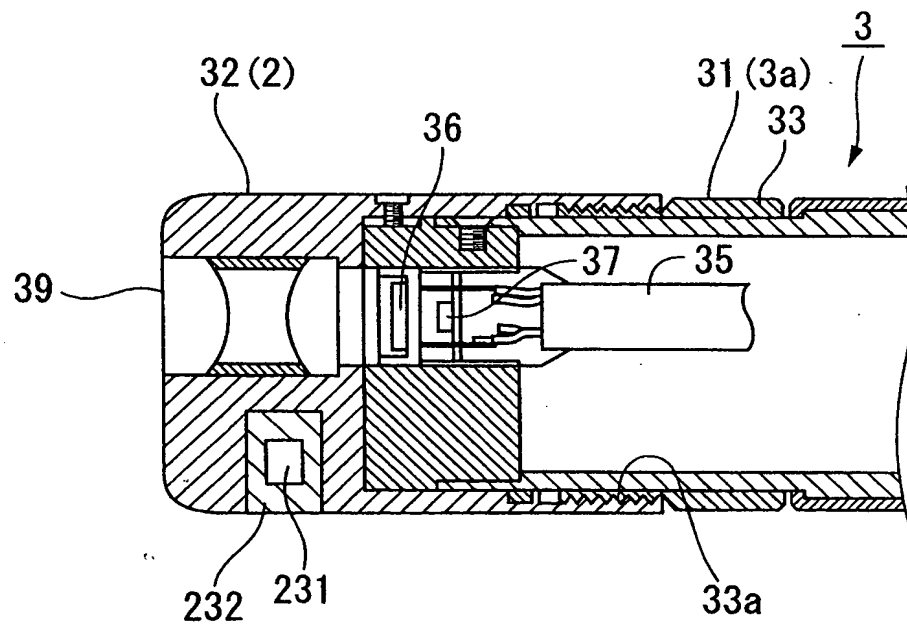


FIG. 31

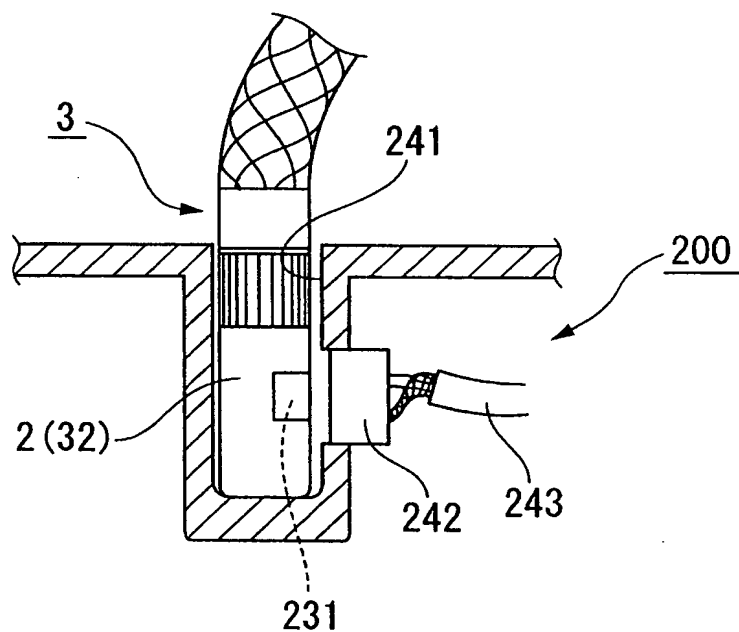


FIG. 32

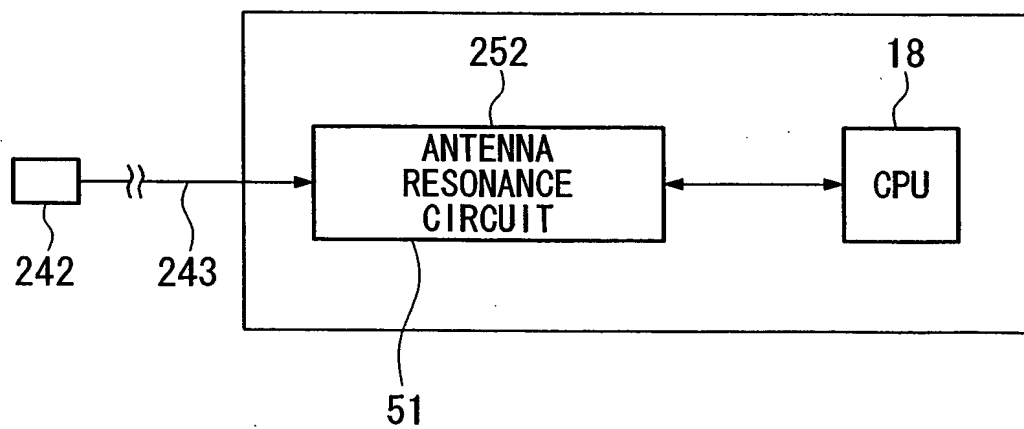


FIG. 33

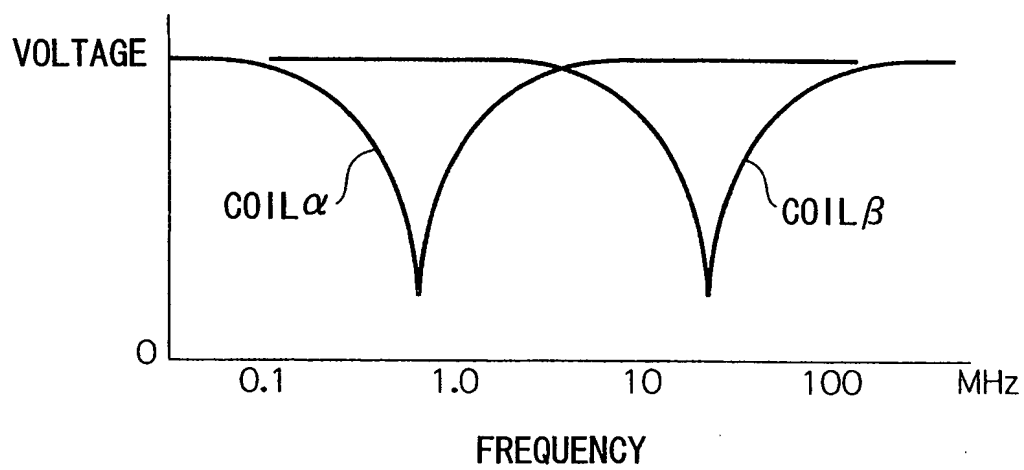


FIG. 34

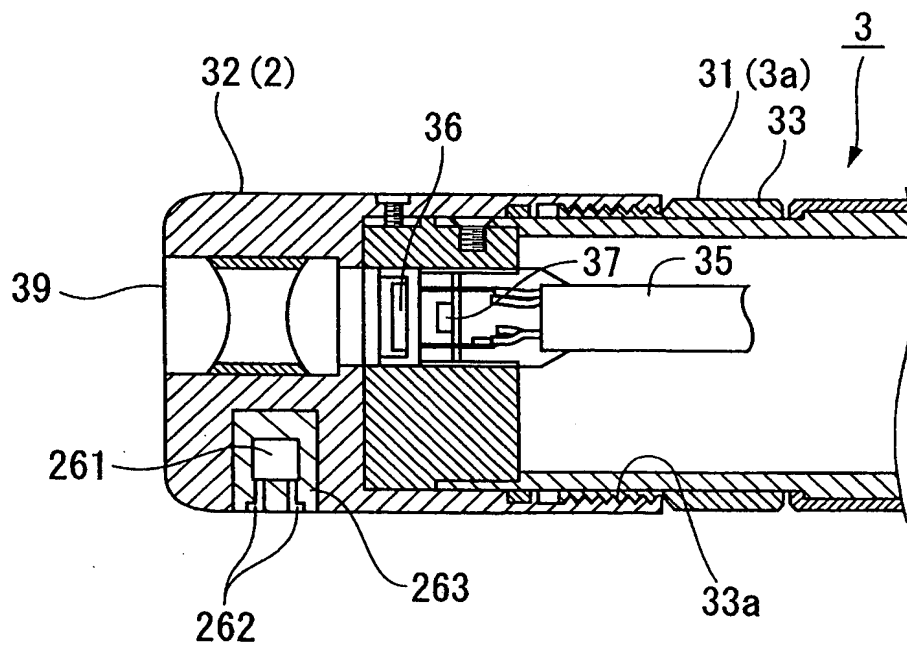


FIG. 35

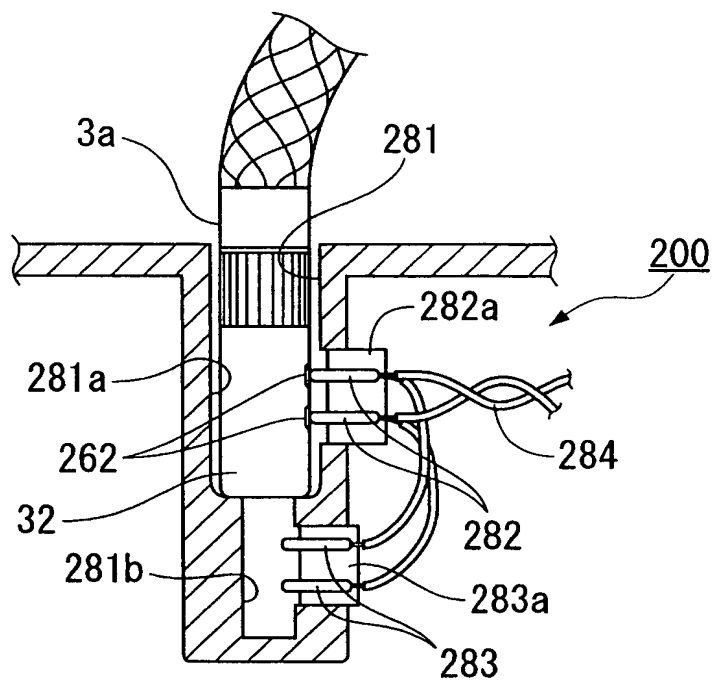


FIG. 36

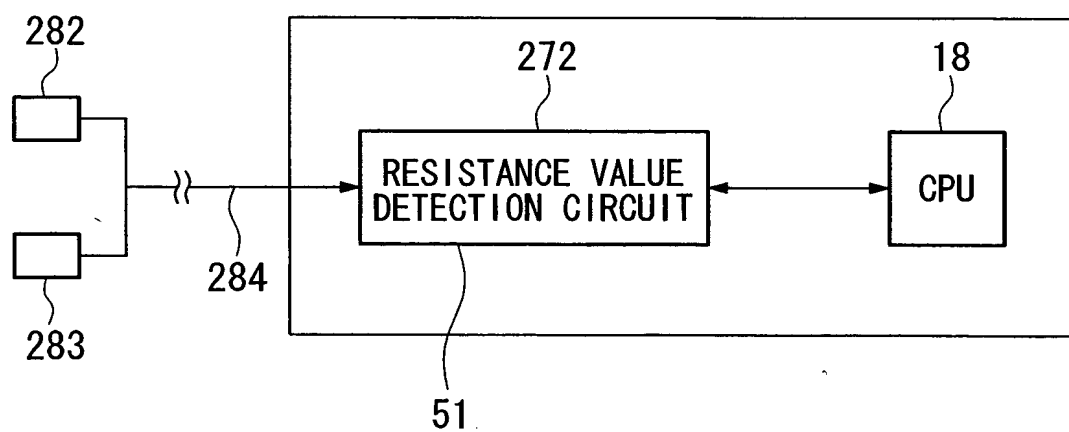


FIG. 37

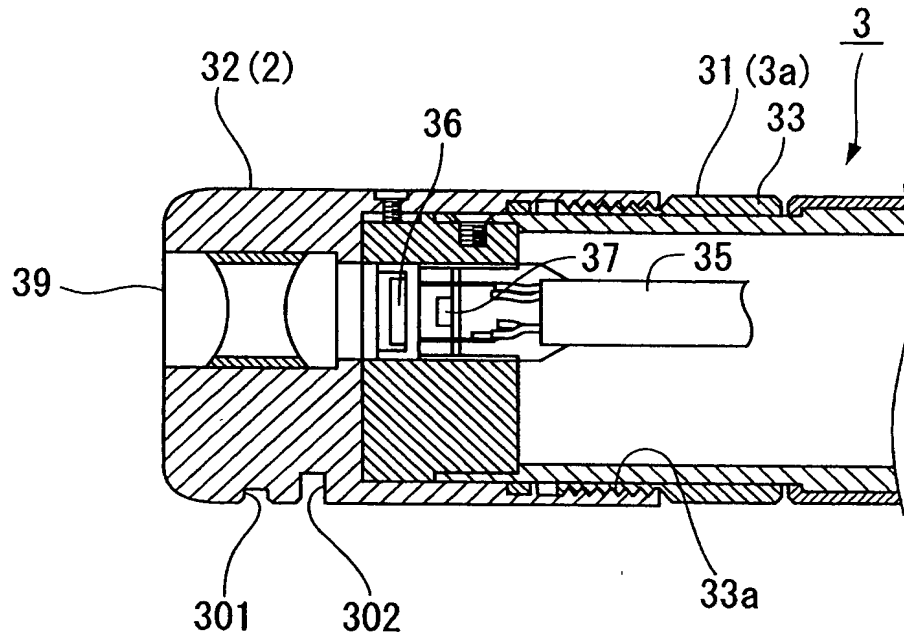


FIG. 38

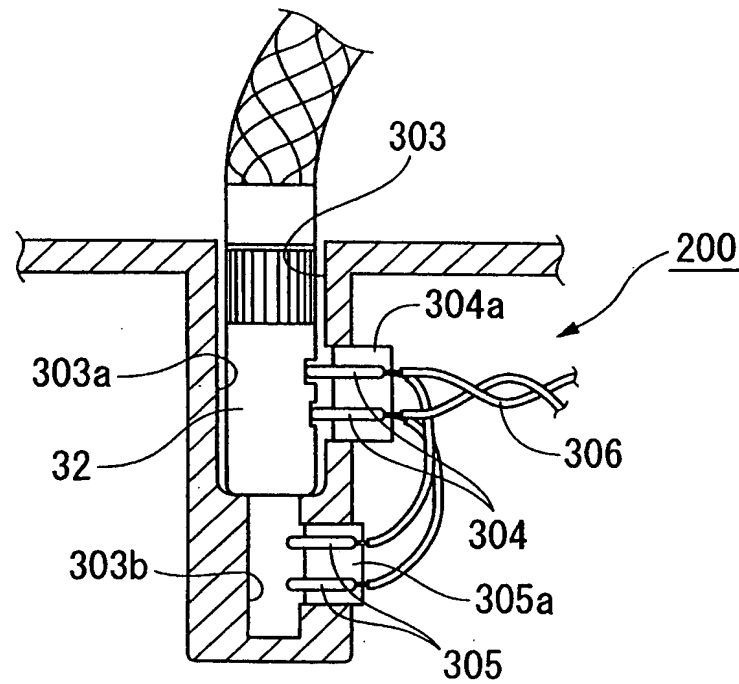


FIG. 39

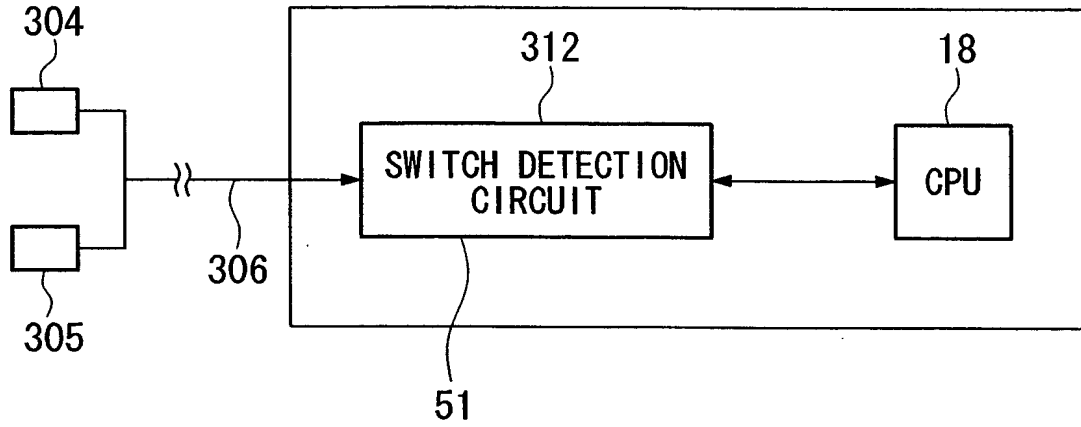


FIG. 40

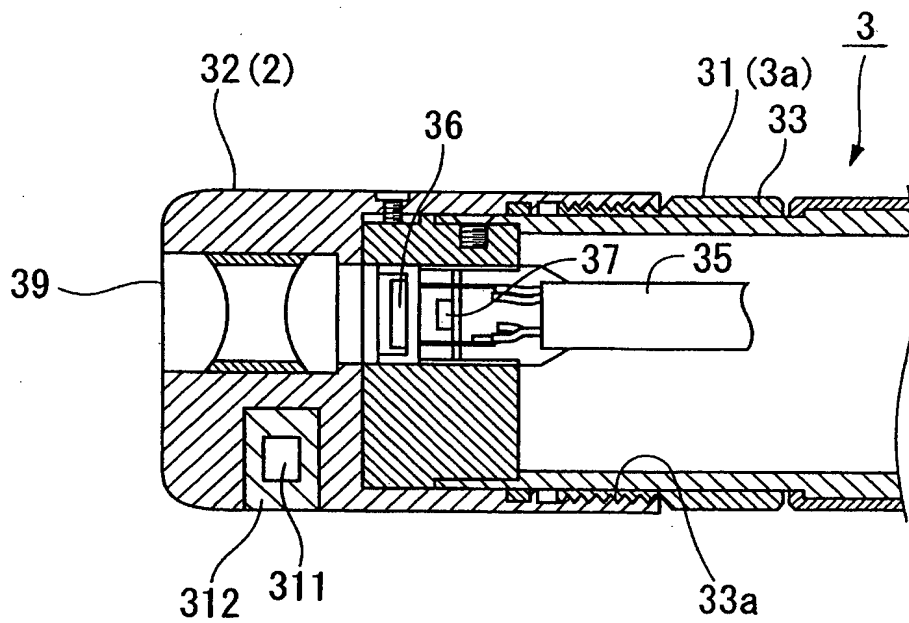


FIG. 41

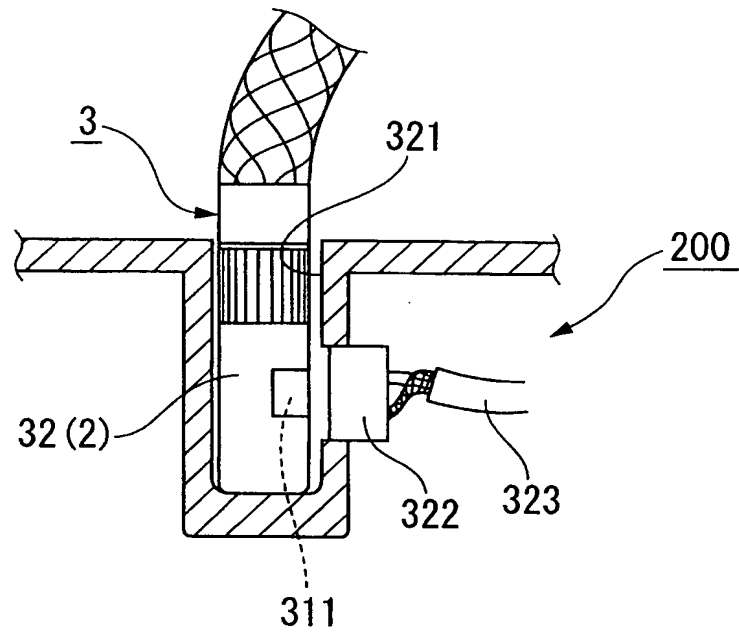


FIG. 42

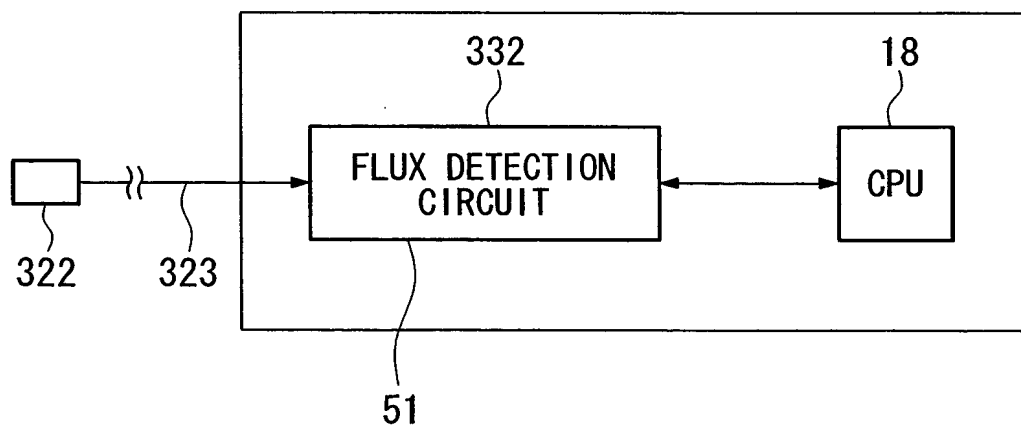


FIG. 43

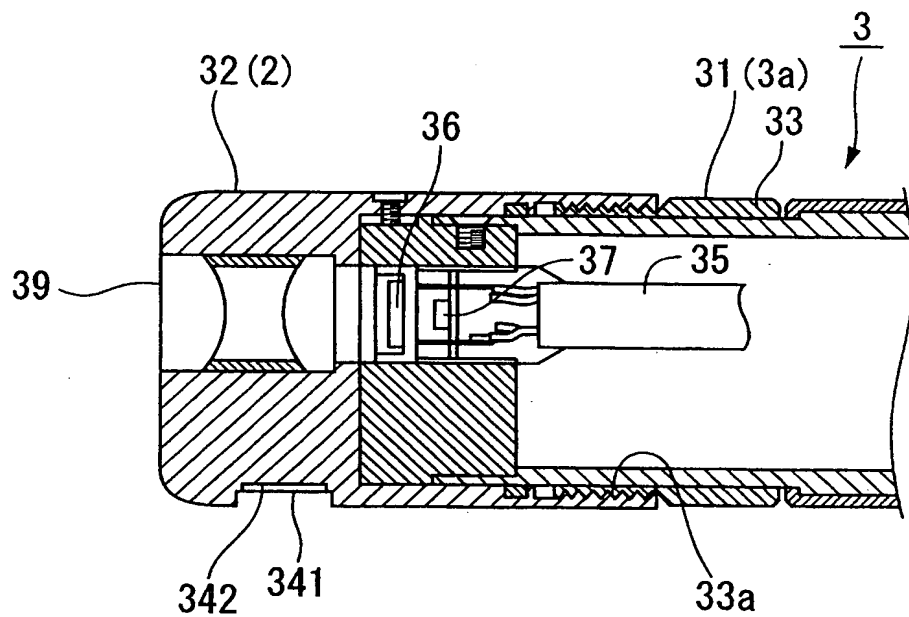


FIG. 44

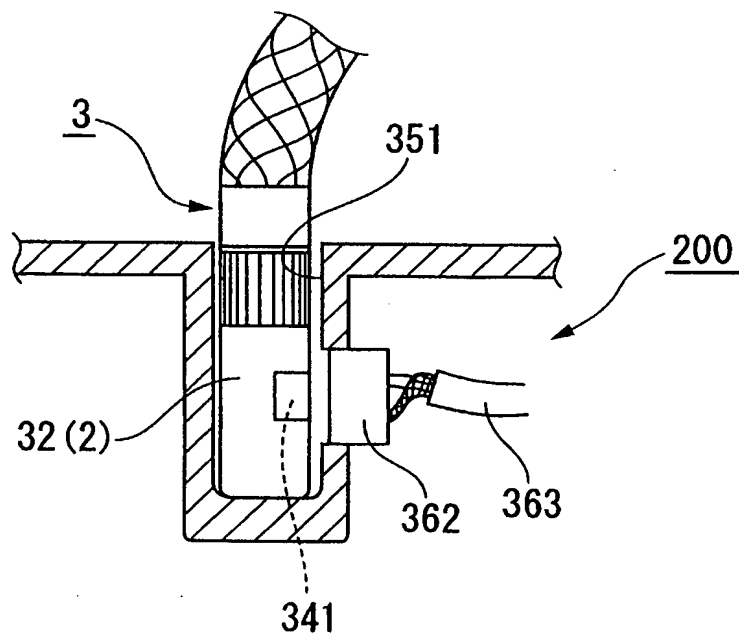




FIG. 45

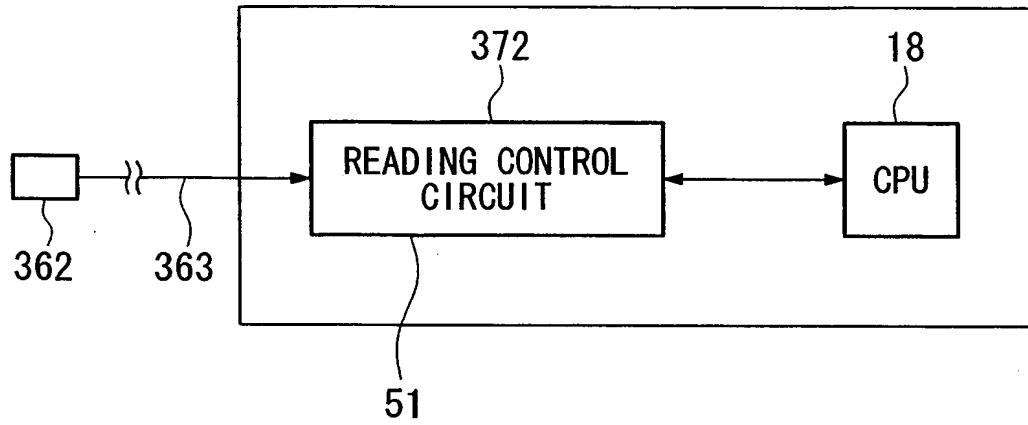


FIG. 46

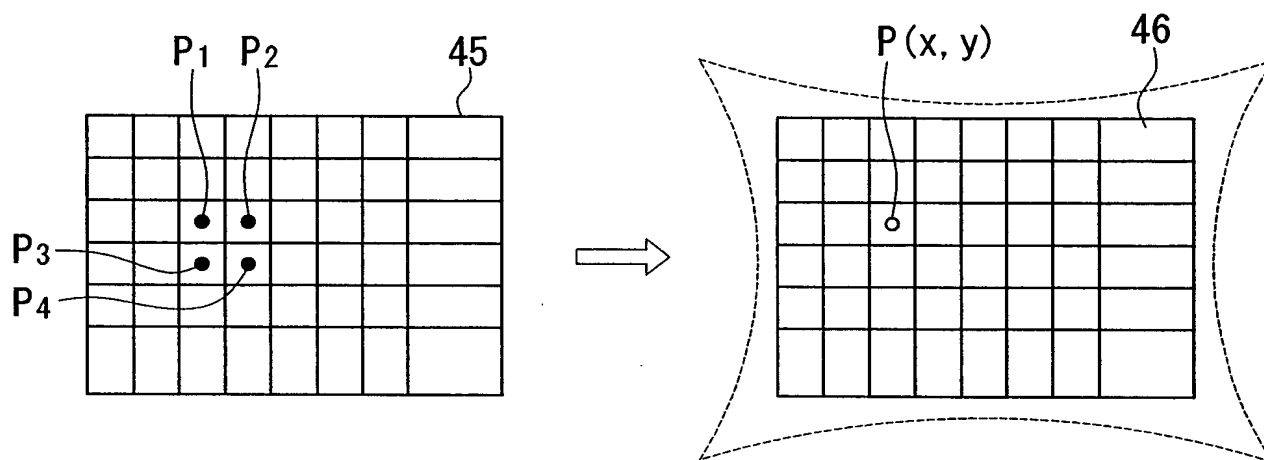


FIG. 47

